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Search Results - Record(s) 1 through 20 of 51 returned.

☐ 1. Document ID: US 20030172290 A1

Using default format because multiple data bases are involved.

L6: Entry 1 of 51

File: PGPB

Sep 11, 2003

PGPUB-DOCUMENT-NUMBER: 20030172290

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030172290 A1

TITLE: Method and system for load balancing an authentication system

PUBLICATION-DATE: September 11, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Newcombe, Christopher Richard	Kirkland	WA	US	
Jones, Paul David	Renton	WA	US	

US-CL-CURRENT: 713/200

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 2. Document ID: US 20030140210 A1

L6: Entry 2 of 51

File: PGPB

Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030140210

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030140210 A1

TITLE: Dynamic and variable length extents

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Testardi, Richard	Berthod	CO	US	

US-CL-CURRENT: 711/203; 711/152, 711/163

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 3. Document ID: US 20030140209 A1

L6: Entry 3 of 51

File: PGPB

Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030140209
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030140209 A1

TITLE: Fast path caching

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Testardi, Richard	Berthod	CO	US	

US-CL-CURRENT: 711/203; 711/152, 711/163

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 4. Document ID: US 20030126371 A1

L6: Entry 4 of 51

File: PGPB

Jul 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030126371
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030126371 A1

TITLE: System and method for performing page table walks on speculative software prefetch operations

PUBLICATION-DATE: July 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Venkatraman, KS	Hillsboro	OR	US	

US-CL-CURRENT: 711/137

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 5. Document ID: US 20030115476 A1

L6: Entry 5 of 51

File: PGPB

Jun 19, 2003

PGPUB-DOCUMENT-NUMBER: 20030115476
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030115476 A1

TITLE: Hardware-enforced control of access to memory within a computer using hardware-enforced semaphores and other similar, hardware-enforced serialization and sequencing mechanisms

PUBLICATION-DATE: June 19, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
McKee, Bret	Fort Collins	CO	US	

US-CL-CURRENT: 713/193

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 6. Document ID: US 20030088609 A1

L6: Entry 6 of 51

File: PGPB

May 8, 2003

PGPUB-DOCUMENT-NUMBER: 20030088609
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030088609 A1

TITLE: Method and system for providing on-line interactivity over a server-client network

PUBLICATION-DATE: May 8, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Guedalia, Isaac David	Bet Shemesh		IL	
Hashkes, Jonathan	Jerusalem		IL	
Guedalia, Jacob Leon	Jerusalem		IL	

US-CL-CURRENT: 718/107

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 7. Document ID: US 20030061258 A1

L6: Entry 7 of 51

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061258
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030061258 A1

TITLE: Method and apparatus for processing an event occurrence for at least one thread within a multithreaded processor

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

h e b b g e e f e h e e f b e

NAME	CITY	STATE	COUNTRY	RULE-47
Rodgers, Dion	Hillsboro	OR	US	
Boggs, Darrell	Aloha	OR	US	
Merchant, Amit	Portland	OR	US	
Kota, Rajesh	Aloha	OR	US	
Hsu, Rachel	Hillsboro	OR	US	
Tiruvallur, Keshavan	Tigard	OR	US	

US-CL-CURRENT: [718/102](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. Des.
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☐ 8. Document ID: US 20020133751 A1

L6: Entry 8 of 51

File: PGPB

Sep 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020133751
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020133751 A1

TITLE: Method and apparatus for fault-tolerance via dual thread crosschecking

PUBLICATION-DATE: September 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nair, Ravi	Briarcliff Manor	NY	US	
Smith, James E.	Madison	WI	US	

US-CL-CURRENT: [714/38](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. Des.
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☐ 9. Document ID: US 20020116600 A1

L6: Entry 9 of 51

File: PGPB

Aug 22, 2002

PGPUB-DOCUMENT-NUMBER: 20020116600
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020116600 A1

TITLE: Method and apparatus for processing events in a multithreaded processor

PUBLICATION-DATE: August 22, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Smith, Lawrence O.	Beaverton	OR	US	
Rodgers, S. Dion	Hillsboro	OR	US	

US-CL-CURRENT: 712/218

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 10. Document ID: US 20020095614 A1

L6: Entry 10 of 51

File: PGPB

Jul 18, 2002

PGPUB-DOCUMENT-NUMBER: 20020095614
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020095614 A1

TITLE: Method and apparatus for disabling a clock signal within a multithreaded processor

PUBLICATION-DATE: July 18, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Rodgers, Dion	Hillsboro	OR	US	
Toll, Bret	Hillsboro	OR	US	
Wood, Aimee	Hillsboro	OR	US	

US-CL-CURRENT: 713/500

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 11. Document ID: US 6681384 B1

L6: Entry 11 of 51

File: USPT

Jan 20, 2004

US-PAT-NO: 6681384
DOCUMENT-IDENTIFIER: US 6681384 B1

TITLE: Multi-threaded break-point

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 12. Document ID: US 6584501 B1

L6: Entry 12 of 51

File: USPT

Jun 24, 2003

US-PAT-NO: 6584501
DOCUMENT-IDENTIFIER: US 6584501 B1

TITLE: Method to display information representing network traffic on a computer display monitor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 13. Document ID: US 6535878 B1

L6: Entry 13 of 51

File: USPT

Mar 18, 2003

US-PAT-NO: 6535878

DOCUMENT-IDENTIFIER: US 6535878 B1

TITLE: Method and system for providing on-line interactivity over a server-client network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☒ 14. Document ID: US 6496925 B1

L6: Entry 14 of 51

File: USPT

Dec 17, 2002

US-PAT-NO: 6496925

DOCUMENT-IDENTIFIER: US 6496925 B1

TITLE: Method and apparatus for processing an event occurrence within a multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 15. Document ID: US 6496909 B1

L6: Entry 15 of 51

File: USPT

Dec 17, 2002

US-PAT-NO: 6496909

DOCUMENT-IDENTIFIER: US 6496909 B1

TITLE: Method for managing concurrent access to virtual memory data structures

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☒ 16. Document ID: US 6493740 B1

L6: Entry 16 of 51

File: USPT

Dec 10, 2002

US-PAT-NO: 6493740

DOCUMENT-IDENTIFIER: US 6493740 B1

TITLE: Methods and apparatus for multi-thread processing utilizing a single-context architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 17. Document ID: US 6408305 B1

L6: Entry 17 of 51

File: USPT

Jun 18, 2002

US-PAT-NO: 6408305

DOCUMENT-IDENTIFIER: US 6408305 B1

TITLE: Access frontier for demand loading pages in object-oriented databases

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☒ 18. Document ID: US 6357016 B1

L6: Entry 18 of 51

File: USPT

Mar 12, 2002

US-PAT-NO: 6357016

DOCUMENT-IDENTIFIER: US 6357016 B1

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for disabling a clock signal within a multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 19. Document ID: US 6269401 B1

L6: Entry 19 of 51

File: USPT

Jul 31, 2001

US-PAT-NO: 6269401

DOCUMENT-IDENTIFIER: US 6269401 B1

TITLE: Integrated computer system and network performance monitoring

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 20. Document ID: US 6240531 B1

L6: Entry 20 of 51

File: USPT

May 29, 2001

US-PAT-NO: 6240531

DOCUMENT-IDENTIFIER: US 6240531 B1

TITLE: System and method for computer operating system protection

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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Term	Documents
(1 NOT 2).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	51

(L1 NOT L2).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	51
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